



Docket No.: 32305-200192
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Takahashi et al.

Art Unit: 2822

Application No: 10/762,347

Examiner: I. M. Soward

Confirmation No: 4869

Filed: January 23, 2004

Atty. Docket No: 32305-200192

For: SEMICONDUCTOR DEVICE HAVING A
CHIP-SIZE PACKAGE

Customer No:

26694

PATENT TRADEMARK OFFICE

TRANSMITTAL OF DRAWING CORRECTION

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Submitted herewith is Figure 18B of corrected drawings for filing in the above-identified Patent application.

The Examiner objected to the drawings for reasons stated in the Office Action (Paper No. 20070329) dated April 10, 2007. The enclosed new drawings correct these informalities.

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Favorable consideration of the enclosed drawings is respectfully requested.

Dated: July 31, 2007

Respectfully submitted,

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